

PATENT
450100-03091

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICATION FOR LETTERS PATENT

TITLE: MAGNETIC TAPE RECORDING APPARATUS AND
METHOD, MAGNETIC TAPE FORMAT, AND
RECORDING MEDIUM THEREFOR

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- 1 -

MAGNETIC TAPE RECORDING APPARATUS AND METHOD, MAGNETIC TAPE
FORMAT, AND RECORDING MEDIUM THEREFOR

BACKGROUND OF THE INVENTION

1. Field of the Invention

The present invention generally relates to magnetic tape recording apparatuses and methods and magnetic tape formats, and to recording media therefor. More specifically, the invention relates to a magnetic tape recording apparatus and method for recording or reading high-quality video data on or from magnetic tape. The invention also relates to a magnetic tape format for use in the above-described magnetic tape recording apparatus and method and to a recording medium for storing a program implementing the above-described method.

2. Description of the Related Art

Along with advanced compression techniques, video data can be compressed and recorded on magnetic tape according to the digital video (DV) system. The format for use in the DV system is defined as a DV format of consumer digital video cassette recorders.

Fig. 1 illustrates the configuration of one track of a related DV format. In the DV format, video data is recorded after being subjected to 24-25 conversion. The numbers of bits shown in Fig. 1 represent numbers after 24-25

conversion has been performed on the video data.

The length of one track is substantially equal to a portion of magnetic tape up to a winding angle of 174 degrees. Outside the one-track portion, a 1250-bit overwrite margin is formed for preventing data from remaining recorded.

The length of one track is 134975 bits when a rotary head is rotated in synchronization with a frequency of $60 \times 1000/1001$ Hz, and is 134850 bits when the rotary head is rotated in synchronization with a frequency of 60 Hz.

In the one-track portion, an insert and track information (ITI) sector, an audio sector, a video sector, and a subcode sector are sequentially disposed in the tracing direction of the rotary head (from the left to right in Fig. 1). A gap G1 is formed between the ITI sector and the audio sector, a gap G2 is formed between the audio sector and the video sector, and a gap G3 is formed between the video sector and the subcode sector.

The length of the ITI sector is 3600 bits. In the ITI sector, a 1400-bit preamble for generating a clock, a start sync area (SSA), and a track information area (TIA) (1920 bits in total are assigned to the SSA and the TIA) are sequentially disposed. In the SSA, the bit string (sync number) required for detecting the position of the TIA is indicated. In the TIA, information indicating whether the

format is a consumer DV format and whether the format is an SP mode or an LP mode, and information concerning the pattern of a one-frame pilot signal is recorded. After the TIA, a 280-bit postamble is disposed. The length of the gap G1 is 625 bits.

The length of the audio sector is 11550 bits. The first 400 bits and the last 500 bits serve as a preamble and a postamble, respectively, and the remaining 10650 bits between the preamble and the postamble is used as audio data. The length of the gap G2 is 700 bits.

The length of the video sector is 113225 bits. The first 400 bits and the last 925 bits serve as a preamble and a postamble, respectively, and the remaining 111900 bits between the preamble and the postamble are used as video data. The length of the gap G3 is 1550 bits.

The length of the subcode sector is 3725 bits when the rotary head is rotated at a frequency of $60 \times 1000/1001$ Hz, and is 3600 bits when the rotary head is rotated at a frequency of 60 Hz. The first 1200 bits and the last 1325 bits or 1200 bits (depending on the frequency of the rotary head as discussed above) serve as a preamble and a postamble, respectively, and the remaining 1200 bits between the preamble and the postamble are used as subcode data.

Fig. 2 illustrates the configuration of the video sector shown in Fig. 1. The video sector is formed of 149

90-byte sync blocks, as shown in Fig. 2. Among the 149 sync blocks, 138 sync blocks are formed of a two-byte sync, a three-byte ID, 77-byte video data, and parity C1 (inner error correcting code). In the remaining 11 sync blocks, 77-byte parity C2 (outer error correcting code) is substituted for the video data.

In the DV format, not only the provision of the gaps G1, G2, and G3, but also a preamble and a postamble are formed for each sector. That is, the so-called "overhead" is large, and a sufficient recording rate cannot be obtained for the real data.

About 25 Mbps are required for recording high-quality video data (hereinafter referred to as "high definition (HD) video data"). In the above-described recording format, however, only 24 Mbps are ensured for the data compressed by the main profile/high level (MP@HL) method in the MPEG system, except for search image data. As a result, although standard-quality video data (hereinafter referred to as the "standard definition (SD) video data") can be recorded, HD video data cannot be recorded after being compressed with the MP@HL or MP@H-14 method.

Additionally, the MPEG method is becoming mainstream for compressing video data. The unit of transport stream (TS) packets of the MPEG-compressed video data is 188 bytes. To dispose such a transport packet in the synch blocks of

the video sector shown in Fig. 2, three sync blocks are required, since each sync block is 77 bytes, (231 bytes (= 77 bytes \times three sync blocks)), thereby causing a redundancy of 43 bytes. Thus, each sync block has about 14 redundancy bytes.

In this manner, according to the DV format, transport packets cannot be efficiently recorded.

SUMMARY OF THE INVENTION

Accordingly, in view of the above background, it is an object of the present invention to efficiently record transport packets.

In order to achieve the above object, according to one aspect of the present invention, there is provided a magnetic tape recording apparatus for recording digital data on tracks of a magnetic tape by using a rotary head. The magnetic tape recording apparatus includes a formatting unit for adding error correcting code to each of first group data including video data, audio data, or search data, and second group data including subcode data related to the first group data, and for formatting the first group data and the second group data so that they are continuously disposed on the tracks of the magnetic tape. A supply unit supplies the data formatted by the formatting unit to the rotary head so as to record the data on the magnetic tape. The formatting

unit continuously disposes 139 sync blocks on each of the tracks, each of the 139 sync blocks having 111 bytes. Among the 139 sync blocks, 121 sync blocks each consist of a two-byte detection pattern for detecting the sync block, three-byte identification information for identifying the sync block, 96-byte main data, and 10-byte inner error correcting code added to the identification information and the main data, and the remaining 18 sync blocks each consist of the two-byte detection pattern, the three-byte identification information, 96-byte outer error correcting code, and the 10-byte inner error correcting code. The outer error correcting code is provided for each group of the 139 sync blocks obtained by dividing 2224 sync blocks contained in sixteen tracks by sixteen planes, 1668 sync blocks contained in twelve tracks by twelve planes, or 1112 sync blocks contained in eight tracks by eight planes. The sync blocks are arranged on the magnetic tape so that the distance between the sync blocks belonging to the identical plane is constant among the planes.

According to another aspect of the present invention, there is provided a magnetic tape recording method for use in a magnetic tape recording apparatus for recording digital data on tracks of a magnetic tape by using a rotary head. The magnetic tape recording method includes: a formatting step of adding error correcting code to each of first group

data including video data, audio data, or search data, and second group data including subcode data related to the first group data, and formatting the first group data and the second group data so that they are continuously disposed on the tracks of the magnetic tape; and a supply step of supplying the data formatted in the formatting step to the rotary head so as to record the data on the magnetic tape. The formatting step continuously disposes 139 sync blocks on each of the tracks, each of the 139 sync blocks having 111 bytes. Among the 139 sync blocks, 121 sync blocks each consist of a two-byte detection pattern for detecting the sync block, three-byte identification information for identifying the sync block, 96-byte main data, and 10-byte inner error correcting code added to the identification information and the main data, and the remaining 18 sync blocks each consist of the two-byte detection pattern, the three-byte identification information, 96-byte outer error correcting code, and the 10-byte inner error correcting code. The outer error correcting code is provided for each group of the 139 sync blocks obtained by dividing 2224 sync blocks contained in sixteen tracks by sixteen planes, 1668 sync blocks contained in twelve tracks by twelve planes, or 1112 sync blocks contained in eight tracks by eight planes. The sync blocks are arranged on the magnetic tape so that the distance between the sync blocks belonging to the identical

plane is constant among the planes.

According to still another aspect of the present invention, there is provided a recording medium for storing a computer readable program for allowing a magnetic tape recording apparatus to record digital data on tracks of a magnetic tape by using a rotary head. The computer readable program includes: a formatting step of adding error correcting code to each of first group data including video data, audio data, or search data, and second group data including subcode data related to the first group data, and formatting the first group data and the second group data so that they are continuously disposed on the tracks of the magnetic tape; and a supply step of supplying the data formatted in the formatting step to the rotary head so as to record the data on the magnetic tape. The formatting step continuously disposes 139 sync blocks on each of the tracks, each of the 139 sync blocks having 111 bytes. Among the 139 sync blocks, 121 sync blocks each consist of a two-byte detection pattern for detecting the sync block, three-byte identification information for identifying the sync block, 96-byte main data, and 10-byte inner error correcting code added to the identification information and the main data, and the remaining 18 sync blocks each consist of the two-byte detection pattern, the three-byte identification information, 96-byte outer error correcting code, and the

10-byte inner error correcting code. The outer error correcting code is provided for each group of the 139 sync blocks obtained by dividing 2224 sync blocks contained in sixteen tracks by sixteen planes, 1668 sync blocks contained in twelve tracks by twelve planes, or 1112 sync blocks contained in eight tracks by eight planes. The sync blocks are arranged on the magnetic tape so that the distance between the sync blocks belonging to the identical plane is constant among the planes.

According to a further aspect of the present invention, there is provided a format of a magnetic tape having tracks on which digital data is recorded by using a rotary head. The format includes error correcting code added to each of first group data including video data, audio data, or search data, and second group data including subcode data related to the first group data. The first group data and the second group data are formatted so that they are continuously disposed on the tracks of the magnetic tape. 139 sync blocks, each of the 139 sync blocks having 111 bytes, are disposed on each of the tracks. Among the 139 sync blocks, 121 sync blocks each consist of a two-byte detection pattern for detecting the sync block, three-byte identification information for identifying the sync block, 96-byte main data, and 10-byte inner error correcting code added to the identification information and the main data,

and the remaining 18 sync blocks each consist of the two-byte detection pattern, the three-byte identification information, 96-byte outer error correcting code, and the 10-byte inner error correcting code. The outer error correcting code is provided for each group of the 139 sync blocks obtained by dividing 2224 sync blocks contained in sixteen tracks by sixteen planes, 1668 sync blocks contained in twelve tracks by twelve planes, or 1112 sync blocks contained in eight tracks by eight planes. The sync blocks are arranged on the magnetic tape so that the distance between the sync blocks belonging to the identical plane is constant among the planes.

According to a yet further aspect of the present invention, there is provided a magnetic tape recording apparatus for recording digital data on tracks of a magnetic tape by using a rotary head. The magnetic tape recording apparatus includes a formatting unit for adding error correcting code to each of first group data including video data, audio data, or search data, and second group data including subcode data related to the first group data, and for formatting the first group data and the second group data so that they are continuously disposed on the tracks of the magnetic tape. A supply unit supplies the data formatted by the formatting unit to the rotary head so as to record the data on the magnetic tape. The formatting unit

continuously disposes 141 sync blocks on each of the tracks, each of the 141 sync blocks having 111 bytes. Among the 141 sync blocks, 123 sync blocks each consist of a two-byte detection pattern for detecting the sync block, three-byte identification information for identifying the sync block, 96-byte main data, and 10-byte inner error correcting code added to the identification information and the main data, and the remaining 18 sync blocks each consist of the two-byte detection pattern, the three-byte identification information, 96-byte outer error correcting code, and the 10-byte inner error correcting code. The outer error correcting code is provided for each group of the 141 sync blocks obtained by dividing 2256 sync blocks contained in sixteen tracks by sixteen planes, 1692 sync blocks contained in twelve tracks by twelve planes, or 1128 sync blocks contained in eight tracks by eight planes. The sync blocks are arranged on the magnetic tape so that the distance between the sync blocks belonging to the identical plane is constant among the planes.

According to a further aspect of the present invention, there is provided a magnetic tape recording method for use in a magnetic tape recording apparatus for recording digital data on tracks of a magnetic tape by using a rotary head. The magnetic tape recording method includes: a formatting step of adding error correcting code to each of first group

data including video data, audio data, or search data, and second group data including subcode data related to the first group data, and formatting the first group data and the second group data so that they are continuously disposed on the tracks of the magnetic tape; and a supply step of supplying the data formatted in the formatting step to the rotary head so as to record the data on the magnetic tape. The formatting step continuously disposes 141 sync blocks on each of the tracks, each of the 141 sync blocks having 111 bytes. Among the 141 sync blocks, 123 sync blocks each consist of a two-byte detection pattern for detecting the sync block, three-byte identification information for identifying the sync block, 96-byte main data, and 10-byte inner error correcting code added to the identification information and the main data, and the remaining 18 sync blocks each consist of the two-byte detection pattern, the three-byte identification information, 96-byte outer error correcting code, and the 10-byte inner error correcting code. The outer error correcting code is provided for each group of the 141 sync blocks obtained by dividing 2256 sync blocks contained in sixteen tracks by sixteen planes, 1692 sync blocks contained in twelve tracks by twelve planes, or 1128 sync blocks contained in eight tracks by eight planes. The sync blocks are arranged on the magnetic tape so that the distance between the sync blocks belonging to the identical

plane is constant among the planes.

According to a further aspect of the present invention, there is provided a recording medium for storing a computer readable program which allows a magnetic tape recording apparatus to record digital data on tracks of a magnetic tape by using a rotary head. The computer readable program includes: a formatting step of adding error correcting code to each of first group data including video data, audio data, or search data, and second group data including subcode data related to the first group data, and formatting the first group data and the second group data so that they are continuously disposed on the tracks of the magnetic tape; and a supply step of supplying the data formatted in the formatting step to the rotary head so as to record the data on the magnetic tape. The formatting step continuously disposes 141 sync blocks on each of the tracks, each of the 141 sync blocks having 111 bytes. Among the 141 sync blocks, 123 sync blocks each consist of a two-byte detection pattern for detecting the sync block, three-byte identification information for identifying the sync block, 96-byte main data, and 10-byte inner error correcting code added to the identification information and the main data, and the remaining 18 sync blocks each consist of the two-byte detection pattern, the three-byte identification information, 96-byte outer error correcting code, and the 10-byte inner

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According to a further aspect of the present invention, there is provided a format of a magnetic tape having tracks on which digital data is recorded by using a rotary head. The format includes error correcting code added to each of first group data including video data, audio data, or search data, and second group data including subcode data related to the first group data. The first group data and the second group data are formatted so that they are continuously disposed on the tracks of the magnetic tape. The 141 sync blocks, each of the 141 sync blocks having 111 bytes, are continuously disposed on each of the tracks. Among the 141 sync blocks, 123 sync blocks each consist of a two-byte detection pattern for detecting the sync block, three-byte identification information for identifying the sync block, 96-byte main data, and 10-byte inner error correcting code added to the identification information and

the main data, and the remaining 18 sync blocks each consist of the two-byte detection pattern, the three-byte identification information, 96-byte outer error correcting code, and the 10-byte inner error correcting code. The outer error correcting code is provided for each group of the 141 sync blocks obtained by dividing 2256 sync blocks contained in sixteen tracks by sixteen planes, 1692 sync blocks contained in twelve tracks by twelve planes, or 1128 sync blocks contained in eight tracks by eight planes. The sync blocks are arranged on the magnetic tape so that the distance between the sync blocks belonging to the identical plane is constant among the planes.

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135 sync blocks on each of the tracks, each of the 135 sync blocks having 114 bytes. Among the 135 sync blocks, 118 sync blocks each consist of a two-byte detection pattern for detecting the sync block, three-byte identification information for identifying the sync block, 99-byte main data, and 10-byte inner error correcting code added to the identification information and the main data, and the remaining 17 sync blocks each consist of the two-byte detection pattern, the three-byte identification information, 99-byte outer error correcting code, and the 10-byte inner error correcting code. The outer error correcting code is provided for each group of the 135 sync blocks obtained by dividing 2160 sync blocks contained in sixteen tracks by sixteen planes, 1620 sync blocks contained in twelve tracks by twelve planes, or 1080 sync blocks contained in eight tracks by eight planes. The sync blocks are arranged on the magnetic tape so that the distance between the sync blocks belonging to the identical plane is constant among the planes.

According to a further aspect of the present invention, there is provided a magnetic tape recording method for use in a magnetic tape recording apparatus for recording digital data on tracks of a magnetic tape by using a rotary head. The magnetic tape recording method includes: a formatting step of adding error correcting code to each of first group

data including video data, audio data, or search data, and second group data including subcode data related to the first group data, and formatting the first group data and the second group data so that they are continuously disposed on the tracks of the magnetic tape; and a supply step of supplying the data formatted in the formatting step to the rotary head so as to record the data on the magnetic tape. The formatting step continuously disposes 135 sync blocks on each of the tracks, each of the 135 sync blocks having 114 bytes. Among the 135 sync blocks, 118 sync blocks each consist of a two-byte detection pattern for detecting the sync block, three-byte identification information for identifying the sync block, 99-byte main data, and 10-byte inner error correcting code added to the identification information and the main data, and the remaining 17 sync blocks each consist of the two-byte detection pattern, the three-byte identification information, 99-byte outer error correcting code, and the 10-byte inner error correcting code. The outer error correcting code is provided for each group of the 135 sync blocks obtained by dividing 2160 sync blocks contained in sixteen tracks by sixteen planes, 1620 sync blocks contained in twelve tracks by twelve planes, or 1080 sync blocks contained in eight tracks by eight planes. The sync blocks are arranged on the magnetic tape so that the distance between the sync blocks belonging to the identical

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According to a further aspect of the present invention, there is provided a recording medium for storing a computer readable program which allows a magnetic tape recording apparatus to record digital data on tracks of a magnetic tape by using a rotary head. The computer readable program includes: a formatting step of adding error correcting code to each of first group data including video data, audio data, or search data, and second group data including subcode data related to the first group data, and formatting the first group data and the second group data so that they are continuously disposed on the tracks of the magnetic tape; and a supply step of supplying the data formatted in the formatting step to the rotary head so as to record the data on the magnetic tape. The formatting step continuously disposes 135 sync blocks on each of the tracks, each of the 135 sync blocks having 114 bytes. Among the 135 sync blocks, 118 sync blocks each consist of a two-byte detection pattern for detecting the sync block, three-byte identification information for identifying the sync block, 99-byte main data, and 10-byte inner error correcting code added to the identification information and the main data, and the remaining 17 sync blocks each consist of the two-byte detection pattern, the three-byte identification information, 99-byte outer error correcting code, and the 10-byte inner

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According to a further aspect of the present invention, there is provided a format of a magnetic tape having tracks on which digital data is recorded by using a rotary head. The format includes error correcting code added to each of first group data including video data, audio data, or search data, and second group data including subcode data related to the first group data. The first group data and the second group data are formatted so that they are continuously disposed on the tracks of the magnetic tape. 135 sync blocks, each of the 135 sync blocks having 114 bytes, are continuously disposed on each of the tracks. Among the 135 sync blocks, 118 sync blocks each consist of a two-byte detection pattern for detecting the sync block, three-byte identification information for identifying the sync block, 99-byte main data, and 10-byte inner error correcting code added to the identification information and

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According to a further aspect of the present invention, there is provided a magnetic tape recording apparatus for recording digital data on tracks of a magnetic tape by using a rotary head. The magnetic tape recording apparatus includes a formatting unit for adding error correcting code to each of first group data including video data, audio data, or search data, and second group data including subcode data related to the first group data, and for formatting the first group data and the second group data so that they are continuously disposed on the tracks of the magnetic tape. A supply unit supplies the data formatted by the formatting unit to the rotary head so as to record the data on the magnetic tape. The formatting unit continuously disposes

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According to a further aspect of the present invention, there is provided a magnetic tape recording method for use in a magnetic tape recording apparatus for recording digital data on tracks of a magnetic tape by using a rotary head. The magnetic tape recording method comprising: a formatting step of adding error correcting code to each of first group

data including video data, audio data, or search data, and second group data including subcode data related to the first group data, and formatting the first group data and the second group data so that they are continuously disposed on the tracks of the magnetic tape; and a supply step of supplying the data formatted in the formatting step to the rotary head so as to record the data on the magnetic tape. The formatting step continuously disposes 135 sync blocks on each of the tracks, each of the 135 sync blocks having 114 bytes. Among the 135 sync blocks, 118 sync blocks each consist of a two-byte detection pattern for detecting the sync block, three-byte identification information for identifying the sync block, 97-byte main data, and 12-byte inner error correcting code added to the identification information and the main data, and the remaining 17 sync blocks each consist of the two-byte detection pattern, the three-byte identification information, 97-byte outer error correcting code, and the 12-byte inner error correcting code. The outer error correcting code is provided for each group of the 135 sync blocks obtained by dividing 2160 sync blocks contained in sixteen tracks by sixteen planes, 1620 sync blocks contained in twelve tracks by twelve planes, or 1080 sync blocks contained in eight tracks by eight planes. The sync blocks are arranged on the magnetic tape so that the distance between the sync blocks belonging to the identical

plane is constant among the planes.

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According to a further aspect of the present invention, there is provided a format of a magnetic tape having tracks on which digital data is recorded by using a rotary head. The format includes error correcting code added to each of first group data including video data, audio data, or search data, and second group data including subcode data related to the first group data. The first group data and the second group data are formatted so that they are continuously disposed on the tracks of the magnetic tape. 135 sync blocks, each of the 135 sync blocks having 114 bytes, are continuously disposed on each of the tracks. Among the 135 sync blocks, 118 sync blocks each consist of a two-byte detection pattern for detecting the sync block, three-byte identification information for identifying the sync block, 97-byte main data, and 12-byte inner error correcting code added to the identification information and

the main data, and the remaining 17 sync blocks each consist of the two-byte detection pattern, the three-byte identification information, 97-byte outer error correcting code, and the 12-byte inner error correcting code. The outer error correcting code is provided for each group of the 135 sync blocks obtained by dividing 2160 sync blocks contained in sixteen tracks by sixteen planes, 1620 sync blocks contained in twelve tracks by twelve planes, or 1080 sync blocks contained in eight tracks by eight planes. The sync blocks are arranged on the magnetic tape so that the distance between the sync blocks belonging to the identical plane is constant among the planes.

In the aforementioned magnetic tape recording apparatus and method, the recording medium, and the magnetic tape format, the video data may be high definition video data compressed by an MP@HL or MP@H-14 method.

BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1 illustrates the configuration of a track sector of a DV format;

Fig. 2 illustrates the configuration of a video sector shown in Fig. 1;

Fig. 3 is a block diagram illustrating an example of the configuration of a recording system for use in a magnetic tape recording/reading apparatus according to the

present invention;

Fig. 4 illustrates the track format of magnetic tape shown in Fig. 3;

Figs. 5, 6, and 7 illustrate tracking pilot signals to be recorded on the track shown in Fig. 4;

Fig. 8 illustrates the sector arrangement of the track shown in Fig. 4;

Fig. 9 illustrates an example of an ITI preamble of track F0 shown in Fig. 8;

Fig. 10 illustrates an example of an ITI preamble of track F1 shown in Fig. 8;

Fig. 11 illustrates an example of an ITI preamble of track F2 shown in Fig. 8;

Fig. 12 illustrates an example of SSA data of track F0 shown in Fig. 8;

Fig. 13 illustrates an example of SSA data of track F1 shown in Fig. 8;

Fig. 14 illustrates an example of SSA data of track F2 shown in Fig. 8;

Fig. 15 illustrates the configuration of a sync block of a TIA shown in Fig. 8;

Fig. 16 illustrates track information of the TIA shown in Fig. 8;

Fig. 17 illustrates information represented by APT of the TIA shown in Fig. 8;

Fig. 18 illustrates an example of TIA data of track F0 shown in Fig. 8;

Fig. 19 illustrates an example of TIA data of track F1 shown in Fig. 8;

Fig. 20 illustrates an example of TIA data of track F2 shown in Fig. 8;

Fig. 21 illustrates an example of data of an ITI postamble of track F0 shown in Fig. 8;

Fig. 22 illustrates an example of data of an ITI postamble of track F1 shown in Fig. 8;

Fig. 23 illustrates an example of data of an ITI postamble of track F2 shown in Fig. 8;

Figs. 24A, 24B, and 24C illustrate an example of the configuration of the main sector shown in Fig. 8;

Fig. 25 illustrates the configuration of the subcode sector shown in Fig. 8;

Fig. 26 illustrates the pattern of the postamble shown in Fig. 8;

Fig. 27 is a block diagram illustrating an example of the configuration of a reading system for use in the magnetic tape recording/reading apparatus according to the present invention;

Fig. 28 illustrates the relationship between the length of a sync block and the 24-25 conversion cycle;

Fig. 29 illustrates the configuration of error

correcting codes of a sync block;

Fig. 30 illustrates the relationship between the bit error probability and the probability that data cannot be correctly decoded;

Fig. 31 illustrates the configuration of inner error correcting codes of a sync block of the DV format;

Fig. 32 illustrates the parity configuration of the ID of the DV format;

Fig. 33 illustrates an example of the arrangement of sync blocks on a plurality of planes on magnetic tape;

Fig. 34 illustrates another example of the arrangement of sync blocks on a plurality of planes on magnetic tape;

Fig. 35 illustrates interleave processing when the order of the output from a video data compressor is arranged to the order of sync blocks on magnetic tape;

Fig. 36 illustrates interleave processing when the order of the output from the video data compressor is arranged to the order of sync blocks on the planes;

Figs. 37, 38, and 39 illustrate the arrangement of sync blocks on magnetic tape after being interleaved on 16 planes over 16 tracks and being provided with parities;

Figs. 40, 41, and 42 illustrate the arrangement of sync blocks on magnetic tape after being provided with parities and being interleaved on 16 planes over 16 tracks;

Fig. 43 illustrates the error resistance to damaged

tracks caused by burst errors;

Fig. 44 illustrates the error resistance to damaged tracks on one side channel caused by burst errors;

Figs. 45 and 46 illustrate the arrangement of sync blocks on magnetic tape after being interleaved on 12 planes over 12 tracks and being provided with parities;

Figs. 47 and 48 illustrate the arrangement of sync blocks on magnetic tape after being interleaved on 8 planes over 8 tracks and being provided with parities;

Figs. 49A, 49B, and 49C illustrate another example of the configuration of the main sector shown in Fig. 8;

Fig. 50 is a block diagram illustrating another example of the configuration of a recording system for use in a magnetic tape recording/reading apparatus according to the present invention;

Fig. 51 illustrates the track format of magnetic tape shown in Fig. 50;

Fig. 52 is a block diagram illustrating another example of the configuration of a reading system for use in the magnetic tape recording/reading apparatus according to the present invention;

Figs. 53, 54, and 55 illustrate the arrangement of sync blocks on magnetic tape after being interleaved on 16 planes over 16 tracks and being provided with parities;

Figs. 56, 57, and 58 illustrate the arrangement of sync

blocks on magnetic tape after being provided with parities and being interleaved on 16 planes over 16 tracks;

Figs. 59A, 59B, and 59C illustrate still another example of the configuration of the main sector shown in Fig. 8;

Fig. 60 illustrates an example of the sector arrangement of the track shown in Figs. 59A, 59B, and 59C;

Figs. 61A, 61B, and 61C illustrate a further example of the configuration of the main sector shown in Fig. 8; and

Fig. 62 illustrates the relationship between the bit error probability and the probability that data cannot be correctly decoded.

DESCRIPTION OF THE PREFERRED EMBODIMENTS

Fig. 3 illustrates the configuration of a recording system of a magnetic tape recording/reading apparatus to which the present invention is applied. A video data compressor 1 compresses input HD video signals according to an MPEG method, such as MP@HL or MP@H-14. An audio data compressor 2 compresses audio signals corresponding to the HD video signals according to, for example, an audio compression method corresponding to a DV-format compression method. System data, such as auxiliary data or subcode data, is input into a terminal 3 from a controller 13.

A switch 4 suitably selects one of the outputs from

among the video data compressor 1, the audio data compressor 2, and the terminal 3 under the control of the controller 13, and supplies the selected output to an error code/ID adder 5. The error code/ID adder 5 adds an error detecting/correcting code or an ID to the input data and performs interleave processing for 16 tracks. The error code/ID adder 5 then outputs the resulting data to a 24-25 converter 6. The 24-25 converter 6 converts the data in units of 24 bits into data in units of 25 bits by adding one redundant bit, which is selected so that a pilot signal for a tracking operation appears at the highest level.

A sync ITI generator 7 generates sync data to be added to main data (Fig. 24) or subcode (Fig. 25), preamble and postamble data, and ITI data (Fig. 8).

A switch 8 selects one of the outputs from the 24-25 converter 6 and the sync ITI generator 7 under the control of the controller 13, and supplies the selected output to a modulator 9. The modulator 9 randomizes the input data so as to prevent "1"s or "0"s from being consecutive, and also modulates the data according to a method suitable for recording the data on magnetic tape 21 (the same method as that used for the DV format). The modulator 9 then supplies the resulting signal to a parallel-to-serial (P/S) converter 10.

The P/S converter 10 converts the input parallel data

into serial data. An amplifier 11 then amplifies the data input from the P/S converter 10. The amplified data is supplied to a rotary head 12 attached to a rotary drum (not shown), and is then recorded on the magnetic tape 21.

Fig. 4 illustrates the format of tracks which are formed on the magnetic tape 21 by the rotary head 12. The rotary head 12 traces over the magnetic tape 21 in the direction from the bottom right to the upper left in the drawing so as to form tracks which tilt with respect to the longitudinal direction of the magnetic tape 21. The magnetic tape 21 is conveyed from the right to the left direction in the drawing.

The tracks can be divided into F0, F1, and F2 according to the type of pilot signal used for a tracking control operation to be recorded on the tracks. The tracks are formed in the order of F0, F1, F0, F2, F0, F1, F0, and F2.

In track F0, as shown in Fig. 5, neither a pilot signal at frequency f1 nor a pilot signal at frequency f2 is recorded. In contrast, as shown in Fig. 6, in track F1, a pilot signal at frequency f1 is recorded. In track F2, as shown in Fig. 7, a pilot signal at frequency f2 is recorded.

Frequencies f1 and f2 are respectively 1/90 and 1/60 of the recording frequency of a channel bit.

The depth of the notch at frequency f1 or f2 of track F0 is, as shown in Fig. 5, 9 dB. In contrast, the carrier-

to-noise ratio (CNR) of the pilot signal at frequency f_1 or f_2 is, as shown in Fig. 6 or 7, greater than 16 dB and smaller than 19 dB. The depth of the notch at frequency f_1 or f_2 is greater than 3 dB.

The track pattern having the above-described frequency characteristics is the same pattern used in the DV format. Accordingly, magnetic tape, a rotary head, a driving system, a demodulation system, and a control system for use in consumer digital video cassette recorders can be employed in the present invention. The track pitch and the tape speed are similar to those in the DV format.

Fig. 8 illustrates an example of the sector arrangement in each track. The number of bits of the individual elements shown in Fig. 8 are numbers after 24-25 conversion is performed on the data. The length of one track is 134975 bits when the rotary head 12 is rotated at a frequency of $60 \times 1000/1001$ Hz, and is 134850 bits when the rotary head 12 is rotated at a frequency of 60 Hz. The length of one track is equal to a portion of the magnetic tape 21 up to a winding angle of 174 degrees. After one track, a 1250-bit overwrite margin is formed for preventing data from remaining recorded.

In Fig. 8, the rotary head 12 traces over the track from the left to the right. At the head of the track, a 3600-bit ITI sector, which is similar to the counterpart

shown in Fig. 1, is disposed. At the start of the ITI sector, a 1400-bit ITI preamble is disposed. The ITI preamble of track 0 has data, such as that shown in Fig. 9, and the ITI preamble of track 1 has data, such as that shown in Fig. 10. The ITI preamble of track 2 has data, such as that shown in Fig. 11. Based on the data of the ITI preamble, a clock is generated when the data is read from the magnetic tape 21.

Subsequent to the ITI preamble, a 1830-bit SSA is located. The SSA of track F0 is formed of data, such as that shown in Fig. 12, the SSA of track F1 includes data, such as that shown in Fig. 13, and the SSA of track F2 contains data, such as that shown in Fig. 14. The start of the subsequent TIA can be detected by the SSA.

The 90-bit TIA is positioned after the SSA. The TIA is formed of 30 sync blocks, and each block is formed of 30 bits ranging from b29 to b0, as shown in Fig. 15. The same data is recorded in three consecutive sync blocks. Accordingly, the same data is substantially repeated three times in the TIA.

Among the 30 bits (bits b29 through b0), the data shown in Fig. 16 is disposed for bits from b27 to b22, and bits from b17 to b12. That is, APT_2 is disposed in bits b12 and b13, APT_1 is positioned for bits 14 and b15, and APT_0 is positioned for bits 16 and b17.

The type of data recorded on the track in the DV format can be identified by APT_2 , APT_1 , and APT_0 . For example, when the values of APT_2 , APT_1 , and APT_0 are "000", data for a consumer digital video cassette recorder, i.e., DV-format data, is recorded on the track. When the values of APT_2 , APT_1 , and APT_0 are "111", data is not recorded on the track. Accordingly, when the values "111" are detected as APT_2 , APT_1 , and APT_0 , a DV-format-compatible magnetic-tape recording/reading apparatus does not perform a reading operation.

In this embodiment, as shown in Fig. 16, the values "111" are recorded as APT_2 , APT_1 , and APT_0 . As a result, when the magnetic tape 21 shown in Fig. 3 is read by a DV-format-compatible magnetic-tape reading apparatus, a recording operation is not performed. In contrast, when the magnetic tape 21 is read by a HD-signal-compatible magnetic-tape recording/reading apparatus, a reading operation is performed assuming that HD video signal data is recorded on the magnetic tape 21.

As shown in Fig. 16, TP_1 is recorded in bits b22 and b23, while TP_0 is recorded in bits b24 and b25. In the DV format, when the values of TP_1 and TP_0 are "11", the track pitch is 0 for an SP mode pitch. When the values of TP_1 and TP_0 are "10", the track pitch is 1 for an LP mode pitch. When the values of TP_1 and TP_0 are "01", the track pitch is 2. When

the values of TP_1 and TP_0 are "00", the track pitch is 3. In the present invention, the definition of TP_1 and TP_0 is similar to that in the DV format.

In the example shown in Fig. 16, since TP_1 and TP_0 indicate "11", the SP mode is selected.

PF_0 and PF_1 are recorded in bit b26 and b27, respectively. PF stands for a pilot frame, and 0 represents pilot frame 0, and 1 represents pilot frame 1. Pilot frame 0 indicates that track F1 is disposed after track F0 as the first two tracks of the ten tracks forming one frame. Pilot frame 1 indicates that track F2 is disposed after track F0 as the above-described first two tracks.

That is, as discussed with reference to Fig. 4, tracks are formed in the order of F0, F1, F0, F2, F0, F1, F0, and F2. If the first two tracks of one predetermined frame are F0 and F1, the subsequent two tracks can be F0 and F1 or F0 and F2 according to the amount of data of the previous frame. The type of track pattern, i.e., F0 and F1 or F0 and F2, can be represented by the pilot frame.

As stated above, the bits of the TIA sync blocks are randomized so as to prevent a considerably large number of consecutive "1"s or "0"s from occurring. As a result, the TIA data formed of three sync blocks (90 bits), each having bits b29 through b0 shown in Fig. 15, of track F0 can be indicated as shown in Fig. 18. The TIA data of track F1 can

be represented as shown in Fig. 19, and the TIA data of track F2 can be designated as shown in Fig. 20.

After the TIA, as shown in Fig. 8, a 280-bit postamble is disposed. The postamble of track 0 can be indicated as shown in Fig. 21, the postamble of track 1 can be represented as shown in Fig. 22, and the postamble of track 2 can be indicates as shown in Fig. 23. The data of the ITI sector is generated by the sync ITI generator 7.

After the postamble, a 128575-bit main sector is disposed. The structure of the main sector is shown in Fig. 24A.

The main sector is formed of, as shown in Fig. 24A, 139 sync blocks, and each sync block has 888 bits (111 bytes). The first 121 sync blocks each have a 16-bit sync, a 24-bit ID, an eight-bit header, 760-bit main data, and 80-bit parity C1. The sync is generated by the sync ITI generator 7. The ID is added by the error code/ID adder 5. The header includes ID information indicating whether the main data is audio data, video data, search video data, transport stream data, or auxiliary data. The header data is supplied as one type of system data from the controller 13 via the terminal 3.

If the main data is video data, it is supplied from the video data compressor 1. If the main data is audio data, it is supplied from the audio data compressor 2. If the main

data is auxiliary data, it is supplied from the controller 13 via the terminal 3.

The parity C1 is calculated for each sync block from the ID, the header, and the main data by the error code/ID adder 5.

Among the 139 sync blocks, the last 18 sync blocks are formed of the sync, the ID, parity C2, and parity C1. Parity C2 can be calculated based on the header or the main data in the longitudinal direction in Fig. 24A. This calculation is performed in the error code/ID adder 5.

The total amount of the data of the main sector is $888 \text{ bits} \times 139 \text{ sync blocks} = 123432 \text{ bits}$, and becomes 128575 bits after 24-25 conversion. The maximum data rate when the rotary head 12 is rotated in synchronization with 60 Hz is substantially $760 \text{ bits} \times 121 \text{ sync blocks} \times 10 \text{ tracks} \times 30 \text{ Hz} = 27.588 \text{ Mbps}$. This bit rate is sufficient to record MP@HL-compressed or MP@H-14-compressed HD video data, audio compressed data, auxiliary data, and search video data.

Subsequent to the main data, a 1250-bit subcode sector is disposed. The configuration of the subcode sector is shown in Fig. 25.

A one-track subcode sector is formed of 10 subcode sync blocks, and each subcode sync block is formed of a sync, an ID, subcode data, and a parity.

At the head of each subcode sync block of the 1250-bit

(after 24-25 conversion) subcode sector, a 16-bit sync (before 24-25 conversion) is disposed, followed by a 24-bit ID. The sync is generated by the sync ITI generator 7, and the ID is added by the error code/ID adder 5.

After the ID, 40-bit subcode data is located. The subcode data is supplied from the controller 13 via the terminal 3, and includes, for example, a track number and a time code number. Subsequent to the subcode data, a 40-bit parity is added. The parity is added by the error code/ID adder 5.

The 120-bit subcode sync block data before 24-25 conversion becomes 125-bit ($= 120 \times 25/24$) data after 24-25 conversion.

After the subcode sector, a postamble is disposed. In the postamble, a combination of pattern A and pattern B required for generating a clock, for example, that shown in Fig. 26, is recorded. Pattern B has "1"s and "0"s inverted with respect to those of pattern A, and vice versa. By suitably combining pattern A and pattern B, the tracking patterns F0, F1, and F2 shown in Figs. 5, 6, and 7, respectively, can be implemented. The run pattern shown in Fig. 9 represents a pattern after 24-25 conversion is performed by the 24-25 converter 6 shown in Fig. 3. The length of the postamble is 1550 bits when the rotary head 12 is rotated in synchronization with $60 \times 1000/1001$ Hz, and is

1425 bits when the rotary head 12 is rotated in synchronization with 60 Hz.

The operation of the recording system shown in Fig. 3 is as follows. A HD video signal is input, together with search video data (thumbnail data), into the video data compressor 1, and is compressed according to, for example, the MP@HL or MP@H-14 method. The audio signal is input into the audio data compressor 2, and is compressed according to a method similar to a DV-format-compatible method. System data, such as subcode data, auxiliary data, and the header, is supplied to the terminal 3 from the controller 13.

Under the control of the controller 13, the switch 4 appropriately incorporates the video data (including the search video data) output from the video data compressor 1, the audio data output from the audio data compressor 2, and the system data output from the terminal 3, and combines the above-described data and outputs it to the error code/ID adder 5.

The error code/ID adder 5 adds a 24-bit ID to each sync block of the main sector shown in Fig. 24A. The parity C1 shown in Fig. 24A is calculated for each sync block and is added, and instead of the header and the main data, the parity C2 is added to the last 18 sync blocks of the 139 sync blocks.

The error code/ID adder 5 also adds, as shown in Fig.

25, the 24-bit ID for each subcode sync block of the subcode data, and also calculates the 40-bit parity.

The error code/ID adder 5 retains 16 tracks of the main data and interleaves it across 16 tracks (subcode data is not interleaved).

The 24-25 converter 6 converts data in units of 24 bits supplied from the error code/ID adder 5 into data in units of 25 bits. Accordingly, the tracking pilot signal components at frequencies f1 and f2 shown in Figs. 5 through 7 appear at the highest level.

The sync ITI generator 7 adds, as shown in Fig. 24A, a 16-bit sync to each sync block of the main sector. The sync ITI generator 7 also adds, as shown in Fig. 25, a 16-bit sync to each subcode sync block of the subcode sector. Additionally, the ITI generator 7 generates the run pattern of the postamble shown in Fig. 26, and also generates the ITI sector data.

More specifically, the above-described data is added or combined as follows. The controller 13 changes the switch 8 to select between the data output from the sync ITI generator 7 and the data from the 24-25 converter 6, and the switch 8 supplies the selected data to the modulator 9.

The modulator 9 randomizes the input data and also modulates it according to a DV-format-compatible method. The modulated data is then output to the P/S converter 10.

The P/S converter 10 converts the input parallel data into serial data, and supplies it to the rotary head 12 via the amplifier 11. The rotary head 12 records the input data on the magnetic tape 21.

Fig. 27 illustrates the configuration of a reading system for reading data recorded on the magnetic tape 21 as discussed above.

The rotary head 12 reads the data recorded on the magnetic tape 21 and outputs it to an amplifier 41. The amplifier 41 amplifies the input signal and supplies it to an analog-to-digital (A/D) converter 42. The A/D converter 42 converts the input analog signal into a digital signal and supplies it to a demodulator 43. The demodulator 43 derandomizes the data supplied from the A/D converter 42 according to a method corresponding to the randomization method employed by the modulator 9, and also demodulates the derandomized data according to a method corresponding to the modulation method employed by the modulator 9.

A sync ITI detector 44 detects a sync of each sync block of the main sector shown in Fig. 24A, a sync of each subcode sync block of the subcode sector shown in Fig. 25, and the ITI sector shown in Fig. 8 from the demodulated data output from the demodulator 43. The sync ITI detector 44 then supplies the detected syncs to an error corrector/ID detector 46. A 25-24 converter 45 converts the data in

units of 25 bits supplied from the demodulator 43 into data in units of 24 bits in accordance with the 24-25 conversion performed by the 24-25 converter 6, and then outputs the converted data to the error detector/ID converter 46.

The error corrector/ID converter 46 performs error correction, ID detection, and interleave processing based on the syncs input from the sync ITI detector 44. Under the control of a controller 13, a switch 47 outputs the video data (including search video data) to a video data decompressor 48, the audio data to an audio data decompressor 49, and system data, such as subcode data and auxiliary data, to the controller 13 via a terminal 50.

The video data decompressor 48 decompresses the input video data and converts the decompressed digital data into analog data, which is then output as an analog HD video signal. The audio data decompressor 49 decompresses the input audio data and converts the decompressed digital data into analog data, which is then output as an analog audio signal.

The reading operation of the reading system shown in Fig. 27 is as follows. The rotary head 12 reads the data recorded on the magnetic tape 21. The read data is then amplified by the amplifier 41 and is supplied to the A/D converter 42. The analog data is converted into digital data by the A/D converter 42 and is input into the

demodulator 43. The digital data is then derandomized and demodulated by the demodulator 43 in accordance with a derandomization method and a demodulation method corresponding to the randomization method and the modulation method, respectively, performed by the modulator 9 shown in Fig. 3.

The output of the A/D converter 42 is also supplied to a servo circuit (not shown) in which pattern A and pattern B recorded in the postamble (Fig. 26) are read so as to generate a tracking pilot signal, thereby performing the tracking control operation. It should be noted that the tracking control signal is read from the overall track, though a tracking signal component read from the ITI sector appears at the highest level.

The 25-24 converter 45 converts the demodulated data in units of 25 bits into data in units of 24 bits, and outputs it to the error corrector/ID detector 46.

The sync ITI detector 44 detects the syncs of the main sector shown in Fig. 24A or the syncs of the subcode sector shown in Fig. 25 from the data output from the demodulator 43, and supplies the detected syncs to the error corrector/ID detector 46. The error corrector/ID detector 46 stores 16 tracks of the main data and performs deinterleave processing, and also performs error correcting of the main data by using parities C1 and C2 of the main

sector shown in Fig. 24A. The error corrector/ID detector 46 also detects the ID of the main sector and determines whether the data recorded in each sync block is video data, audio data, auxiliary data, or search video data.

The error corrector/ID detector 46 also performs error correcting of the subcode data by using the parity of the subcode sector shown in Fig. 25, and detects the ID so as to determine the type of subcode data, i.e., whether the subcode data represents a track number or a time code number.

The switch 47 supplies the video data and the search video data to the video data decompressor 48 based on the ID detected by the error corrector/ID detector 46. The video data decompressor 48 decompresses the data according to a decompression method corresponding to the compression method employed by the video data compressor 1 shown in Fig. 3, and outputs the decompressed data as the video signal.

The switch 47 outputs the audio data to the audio data decompressor 49. The audio data decompressor 49 decompresses the data according to a decompression method corresponding to the compression method employed by the audio data compressor 2 shown in Fig. 3, and outputs the decompressed data as the audio signal.

The switch 47 also outputs the auxiliary data and subcode data output from the error corrector/ID detector 46 to the controller 13 via the terminal 50.

Details of the configuration of the main sector are further discussed below. As shown in Fig. 24A, each sync block of the main data has 111 bytes (= 888 bits) consisting of a two-byte sync pattern, a three-byte ID, 96-byte main data, and 10-byte parity C1. The data length of the sync block after 24-25 conversion, i.e., 925 bits ($= 111 \times 8 \times 25/24$) are a multiple of 25 bits, and also, 888 bits are a multiple of three bytes (24 bits). As a result, as shown in Fig. 28, the head of a sync block coincides with the start of a 24-25 conversion cycle, thereby facilitating signal processing.

As the inner error correcting code, as shown in Fig. 29, a Galois field $GF(2^8)$ 109-byte Reed-Solomon code (109, 99, 11) is formed of a three-byte ID, 96-byte main data, and 10-byte parity C1. When the bit error probability of a read bit data string recorded on the magnetic tape 21 is indicated by P_b , the Galois field $GF(2^8)$ symbol error probability P_s can be expressed by the following equation.

$$P_s = 1 - (1 - P_b)^8$$

The probability P that the Reed-Solomon code cannot be correctly decoded (impossible to be decoded or is erroneously decoded) can be expressed by the following equation.

$$P = 1 - \sum_{i=0}^t {}_{109}C_i \cdot (P_s)^i \cdot (1 - P_s)^{109-i}$$

Curve A in Fig. 30 indicates the probability P that the Reed-Solomon code cannot be correctly decoded.

For comparison with curve A, the probability that DV format data cannot be correctly decoded is found. In the DV format, as shown in Fig. 31, as the inner error correcting code, a Galois field (2^8) Reed-Solomon code (85, 77, 9) is formed of 77-byte main data and 8-byte parity C1 without including the ID. The probability that the Reed-Solomon code cannot be correctly decoded is expressed by curve B in Fig. 30.

Curve A obtained by the format of the main sector according to the present invention shows that the probability that Reed-Solomon code cannot be correctly decoded when the bit error probability is around 0.0001 is about $1E-09$. In contrast, curve B obtained by the DV format reveals that the above-described probability is about $1E-08$. Thus, the probability P indicated by curve A is smaller than that of curve B by one and half orders of magnitude.

The probability Q that Reed-Solomon code is erroneously corrected is simply determined by the number of parity bits N and can be expressed by the following equation.

$$Q = 1/2^N$$

The number of parity bits in the DV format is 64 ($= 8 \times 8$), and the probability that the Reed-Solomon data is erroneously corrected can be expressed by the following expression.

$$QDV = 5.4E-20$$

In contrast, the number of parity bits in the present invention is 80 ($= 10 \times 8$), and the probability QIN that data is erroneously corrected can be expressed by the following equation.

$$QIN = 8.3E-25$$

That is, according to the present invention, the probability that the Reed-Solomon code is erroneously corrected is reduced by about five orders of magnitudes over the DV format.

Additionally, in the present invention, the ID is included in the inner error correcting code, as shown in Fig. 29. Conversely, as shown in Fig. 31, the ID is not included in the inner error correcting code.

In the DV format, the ID is error-corrected by two-plane Bose-Chaudhuri-Hocquenghem (BCH) code (12, 8, 3). Fig. 32 illustrates ID parities in the DV format. Parities P_0 through P_7 are calculated for two-byte data C_0 through C_{15} , as shown in Fig. 32, resulting in the DV-format ID. In this error correcting, since the minimum Hamming distance is three symbols, an error having three symbols may be

corrected to a wrong code. Additionally, the BCH code is binary code, and the bit rate is merely arranged in the order of No good-OK-No good. Thus, the data may be erroneously corrected.

In contrast, according to the present invention, the three-byte ID is included together with the main data in the Reed-Solomon code, thereby improving the error correcting performance. In terms of the ID, the Reed-Solomon code is substituted for the BCD code, thereby increasing error resistant characteristics compared to the DV format. In terms of the main data, the code length is increased, thereby enhancing the coding efficiency.

By using a Galois field (2^8) Reed-Solomon code (139, 121, 19), bit errors caused by a scratch extending a maximum of 650 μm in the tracking direction can be corrected. Moreover, as will be discussed below, if outer error correcting code is interleaved across a plurality of tracks, for example, 16 tracks, on the magnetic tape 21, errors continuously extending two tracks can be corrected.

Also, in the present invention, sync blocks used for error correction (sync blocks having parity C2) are disposed toward the front in the tracing direction of the rotary head 12 (in the direction from the bottom to up in Fig. 24A), i.e., at the head of the track. The head of the track is vulnerable to reading errors since it is frequently in

contact with the rotary head 12. In the format shown in Fig. 24A, however, sync blocks having parity C2 are disposed at the head of the track, and sync blocks including the main data are disposed toward the end. As a result, the probability that the sync blocks having the main data cannot be decoded can be lower than the probability that the sync blocks having parity C2 cannot be decoded.

Alternatively, as shown in Fig. 24B, parity C2 may be located at the uppermost portion (toward the end of the track).

Alternatively, as shown in Fig. 24C, parity C2 may be divided and located near the head and near the end of the track. In the example shown in Fig. 24C, 9 sync blocks having parity C2 are disposed near the head of the track, and another 9 sync blocks having parity C2 are located near the end of the track. The split ratio of sync blocks having parity C2 does not have to be 1:1, as shown in Fig. 24C.

In the present invention, for enhancing error resistance to a scratch extending over more than one track, error correcting codes are shuffled over a plurality of tracks, and are then recorded on the magnetic tape 21. Accordingly, in N tracks, N-plane error correcting codes are formed. On one plane, Galois field $GF(2^8)$ Reed-Solomon codes (139, 121, 19) are used. On the magnetic tape 21, the distance between adjacent sync blocks belonging to the same

plane is fixed so that the resistance to a scratch extending in the longitudinal direction of the track can be consistent regardless of the location of the scratch in the tracking direction.

Fig. 33 illustrates an example of the arrangement of sync blocks on the magnetic tape 21 when error correcting codes are interleaved over eight-plane eight tracks. In this example, the first-plane through eighth-plane sync blocks are sequentially disposed in the top to bottom direction from the leftmost track. After an eighth-plane sync block is disposed, another first-plane sync block is positioned again. When a sync block is located at the bottommost portion of one track (in the example shown in Fig. 33, when the 22nd sync block is disposed), the subsequent-plane sync block is positioned at the uppermost portion of a track right adjacent to the previous track. In this manner, sync blocks are disposed for the eight-plane eight tracks.

A sync block 81 and a sync block 82 belong to the same plane (second plane), and are separated from each other by eight blocks. A sync block 83 and a sync block 84 also belong to the same plane (first plane), and are also separated from each other by eight blocks. In this manner, the distance between adjacent sync blocks belonging to the same plane is constant.

In Fig. 33, it is assumed that a scratch 71 or 72 is

formed in the longitudinal direction (in the vertical direction in the drawing) for the eight tracks. Such a scratch 71 or 72 extends over only 6 sync blocks for each track. Accordingly, only one sync block for each plane is missing, which can be sufficiently corrected.

That is, a scratch formed with the same length among the tracks (i.e., the same height in Fig. 33) can be equally corrected with the same result regardless of where the scratch is formed on the track.

In the example shown in Fig. 33, the continuity of the planes between adjacent tracks is ensured. Thus, the error correcting performance can be best exhibited for temporally continuous burst errors caused by an extraneous substance attached to the magnetic tape 21 or spontaneous clogging at spliced portions of the magnetic tape 21 caused during a recording operation.

Fig. 34 illustrates another example of the arrangement of sync blocks on the magnetic tape 21 when error correcting codes are interleaved over eight-plane eight tracks. In this example, the arrangement of sync blocks is the same between two adjacent tracks. The first-plane through eighth-plane sync blocks are sequentially disposed from the top to the bottom of the two adjacent tracks, and when the sync blocks reach the bottommost portions of the tracks, the subsequent sync blocks are positioned at the uppermost

portions of the tracks, which are located two tracks away from the previous tracks.

According to the arrangement of the sync blocks shown in Fig. 34, the performance in correcting a scratch extending in the longitudinal direction of the track is similar to that exhibited when the sync blocks are arranged in the example shown in Fig. 33. However, according to the arrangement shown in Fig. 34, the error correcting performance is most effective for clogging on one side channel occurring during a reading operation.

For example, when Reed-Solomon codes are interleaved on 16 planes over 16 tracks, there are two approaches to arrange the sync blocks and to add parities by the error code/ID adder 5. In one method, as shown in Fig. 35, the sync blocks are arranged as follows. The 16 planes are formed while error correcting codes are interleaved, and parities are then added. Thereafter, the sync blocks are arranged in the order of the arrangement on the magnetic tape 21.

More specifically, in the example shown in Fig. 35, when the data output from the video data compressor (MPEG encoder) 1 is temporally in the order D0, D1, D1, D2, D3, and so on, the data input into a memory 91 are temporally assigned to planes 91-1 through 91-16. In other words, data D0 through D15 are sequentially disposed in the planes 91-1

through 91-16, respectively. Then, the subsequent data D16 is again disposed in the plane 91-1. In this manner, data are sequentially positioned in the planes 91-1 through 91-16.

After the planes are formed in the memory 91, an outer parity (parity C2) is calculated and added for each of the planes 91-1 through 91-16 by an outer parity adder 92.

The data with the outer parities are then supplied to a memory 93. In the memory 93, the data is sequentially arranged in the same order arranged by the video data compressor 1 (in the order D0, D1, D2, and so on), and 139 pieces of data (121 pieces of data and 18 parities) are stored for each of a first memory area 93-1 through a sixteenth memory area 93-16. That is, for example, data D0, D1, D2, ..., D120 and the corresponding parities P0, P1, ..., P17 are stored in the first memory area 93-1. Data D121, D122, ..., D241, and the corresponding parities are stored in the second memory area 93-2.

According to the priority concerning which type of error the error correcting performance is used, the 16 data groups are read from the memory 93 according to either method shown in Fig. 33 or 34, and are supplied to an inner parity adder 94. In the inner parity adder 94, an inner parity (parity C1) is added. Then, the order of the data output from the video data compressor 1 can be rearranged to the order of data on the magnetic tape 21. With this

arrangement, a continuous error, which may occur during a reading operation, becomes temporally continuous when it is input into the video data decompressor 48. This method is effective on occasions, such as when there are continuous errors within a B picture. A B picture is not referred to by other pictures (I picture and P picture) according to the MPEG method, and thus, errors occurring in a B picture do not travel to an I picture or a P picture. On the other hand, it is very likely that errors occur for a small amount of data.

As another approach to arrange the sync blocks and to add parities, as shown in Fig. 36, after parities are added, the data are rearranged by being shuffled in the order of the arrangement on the magnetic tape 21. More specifically, as shown in Fig. 36, the data D0, D1, D2, and so on, output from the video data compressor 1 are assigned in units of 121 data to 16 different planes in the temporal order in the memory 91. For example, the first data D0 through 121st data D120 are stored in the first memory area 91-1, and the 122nd data D121 through the 242nd data D241 are stored in the second memory area 91-2. In the same manner, the subsequent data are stored in the memory areas 91-3 through 91-16.

Upon completing the formation of the 16 planes in the memory 91, an outer parity is added for each plane by the

outer parity adder 92. The data with the outer parities are then assigned to the first through sixteenth groups in the memory 93 so that the distance between adjacent data is set to be constant among the planes. For example, the data D0 is stored in the first memory area 93-1, and the subsequent data D1 is stored in the second memory area 93-2. Similarly, the data D15 is stored in the sixteenth memory area 93-16. Then, the 17th data D16 is again stored in the first memory area 93-1, and the 18th data D17 is stored in the second memory area 93-2.

As described above, according to the priority concerning which type of error the error correcting performance is used, the sixteen data groups are read from the memory 93 group by group according to either method shown in Fig. 33 or 34, and are supplied to the inner parity adder 94. An inner parity is added to each data by the inner parity adder 94. That is, an inner parity is added to the data and the outer parity stored in the first memory area 93-1, and another inner parity is added to the data and the outer parity stored in the second memory area 93-2. The same applies to the subsequent data and the outer parities.

In this manner, the order of the data output from the video compressor 1 is rearranged to the order of the sync blocks on the individual planes. Thus, continuous errors may occur on the track during a reading operation in the

following manner. When the data is input into the video data decompressor 48, it is very unlikely that errors continue in time, but errors may occur at regular intervals among 16 planes. In this case, in the MPEG method, errors occur over a plurality of pictures, and by cross-referring to pictures, an error may propagate more easily compared to the method shown in Fig. 35. On the other hand, errors will seldom occur for a small amount of data.

Accordingly, the arrangements of sync blocks on the magnetic tape 21 can be classified into the following four types according to the resistance to continuous errors and the distribution of uncorrectable errors:

(1) resistant to continuous errors caused by an extraneous substance on the tape, and uncorrectable errors temporally concentrating on one portion;

(2) resistant to continuous errors caused by an extraneous substance on the tape, and uncorrectable errors being temporally distributed;

(3) resistant to continuous errors caused by clogging on one side channel, and uncorrectable errors temporally concentrating on one portion; and

(4) resistant to continuous errors caused by clogging on one side channel, and uncorrectable errors being temporally distributed.

Figs. 37 through 39 illustrate an example of the type

(1) arrangement of sync blocks when error correcting codes are interleaved on 16 planes over 16 tracks. In contrast, Figs. 40 through 42 illustrate an example of the type (2) arrangement of sync blocks when error correcting codes are interleaved on 16 planes over 16 tracks.

In Figs. 37 through 42, M_i ($i = 1, 2, \dots, 16$) indicates the plane number, D_j ($j = 1, 2, \dots$) represents sync block data, P_k ($k = 1, 2, \dots$) designates a parity, and j and k represent serial numbers.

In the examples shown in Figs. 37 through 42, outer parities (parities C2) are disposed toward the end of the track.

When Reed-Solomon codes are formed on 16 planes over 16 tracks and interleaved under the above-described condition (1) or (2), continuous errors extending over two tracks and ten sync blocks can be corrected, as shown in Fig. 43. Similarly, when Reed-Solomon codes are formed under the above-described condition (3) or (4), continuous errors over two tracks and four sync blocks caused by clogging on one side channel can be corrected, as shown in Fig. 44.

The resistance to continuous errors can be varied even by using the same Reed-Solomon codes. For example, Figs. 45 and 46 illustrate an example of the arrangement of sync blocks in which Reed-Solomon codes are interleaved on 12 planes over 12 tracks. Figs. 47 and 48 illustrate an

example of the arrangement of sync blocks in which Reed-Solomon codes are interleaved on 8 planes over 8 tracks.

In the example shown in Figs. 45 and 46, burst errors extending over one track and 77 sync blocks can be corrected, while clogging on one side channel for one track and 63 sync blocks can be corrected.

In contrast, in the example shown in Figs. 47 and 48, burst errors extending for one track and five syncs can be corrected, while clogging on one side channel for one track and two sync blocks can be corrected.

Figs. 49A, 49B, and 49C illustrate another example of the configuration of the main sector. As in the example shown in Figs. 24A, 24B, and 24C, the length of the main sector is 111 bytes. However, the number of sync blocks is 141, and thus, the number of sync blocks forming the main data is increased to 123, which is greater than that shown in Figs. 24A through 24C by two. The other configurations of this example are similar to those shown in Figs. 24A through 24C.

Fig. 50 illustrates an example of a recording system of a magnetic tape recording/reading apparatus when the main sector is configured as shown in Figs. 49A through 49C. A sync generator 7A is substituted for the sync ITI generator 7 shown in Fig. 3.

The sync generator 7A generates sync data to be added

to the main data (Figs. 49A through 49C) or the subcode (Fig. 25), and preamble and postamble data. The other configurations are similar to those shown in Fig. 3.

Fig. 51 illustrates an example of the sector arrangement in each track of the magnetic tape 21 shown in Fig. 50. The numbers of bits representing the lengths of the individual elements shown in Fig. 51 are numbers after 24-25 conversion is performed. The length of one track is 134975 bits when the rotary head 12 is rotated at a frequency of $60 \times 1000/1001$ Hz, and is 134850 bits when the rotary head 12 is rotated at a frequency of 60 Hz. The length of one track is equal to a portion of the magnetic tape 21 up to a winding angle of 174 degrees. Outside the one-track portion, a 1250-bit overwrite margin is formed for preventing data from remaining recorded.

In Fig. 51, the rotary head 12 traces over the track from the left to the right. At the head of the track, a 1800-bit preamble is disposed. As in the postamble after the subcode sector shown in Fig. 8, in this preamble shown in Fig 51, data required for generating a clock, such as a combination of pattern A and pattern B shown in Fig. 26, is recorded. In pattern A and pattern B, "0"s and "1"s are inverted with respect to each other. By suitably combining these patterns, the tracking patterns F0, F1, and F2 shown in Figs. 5, 6, and 7, respectively, can be implemented. The

run pattern shown in Fig. 26 represents a pattern after 24-25 conversion is performed by the 24-25 converter 6 shown in Fig. 50.

After the 1800-bit preamble, a 134850-bit main sector is disposed. The structure of the main sector is shown in Fig. 49A.

As shown in Fig. 49A, the main sector is formed of 141 sync blocks, and the length of each sync block is 888 bits (111 bytes).

The first 123 sync blocks are each formed of a two-byte (16-bit) sync, a three-byte (24-bit) ID, 96-byte (768-bit) main data, and 10-byte (80-bit) parity C1. The syncs are generated by the sync generator 7A. The ID is added by the error code/ID adder 5.

When the main data is video data, it is supplied from the video data compressor 1. When the main data is audio data, it is supplied from the audio data compressor 2. When the main data is auxiliary data, it is supplied from the controller 13 via the terminal 3.

The parity C1 is calculated from the ID and the main data for each sync block by the error code/ID adder 5, and is then added.

Among the 141 sync blocks, the last 18 sync blocks are each formed of a sync, an ID, parity C2, and parity C1. The parity C2 is calculated based on the main data in the

longitudinal direction in Fig. 49A. This calculation is performed in the error code/ID adder 5.

The total amount of data of the main sector is 888 bits \times 141 sync blocks = 125208 bits, and becomes 130425 bits after 24-25 conversion. The maximum data rate when the rotary head 12 is rotated in synchronization with 60 Hz is substantially 768 bits \times 123 sync blocks \times 10 tracks \times 30 Hz = 28.339 Mbps. This bit rate is sufficient to record MP@HL-compressed or MP@H-14-compressed HD video data, audio compressed data, auxiliary data, and search video data.

Subsequent to the main data, a 1250-bit subcode sector is disposed. The configuration of the subcode sector is shown in Fig. 25.

After the subcode sector, a postamble is located. The postamble, as well as the preamble, can be recorded by a combination of pattern A and pattern B shown in Fig. 26. The length of the postamble is 1500 bits when the rotary head 12 is rotated in synchronization with $60 \times 1000/1001$ Hz, and is 1375 bits when the rotary head 12 is rotated in synchronization with 60 Hz.

The operation of the recording system shown in Fig. 50 is similar to that of the counterpart shown in Fig. 3, and an explanation thereof will thus be omitted.

Fig. 52 illustrates an example of the configuration of a reading system for reading the data recorded on the

magnetic tape 21 shown in Fig. 50. This reading system is similar to that shown in Fig. 27, except that, instead of the sync ITI detector 44 shown in Fig. 27, a sync detector 44A is used.

The sync detector 44A detects the sync of each sync block of the main sector shown in Fig. 49A and the sync of each subcode sync block of the subcode sector shown in Fig. 25 from the demodulated data output from the demodulator 43, and supplies the detected syncs to the error detector/ID corrector 46.

The operation of the reading system shown in Fig. 52 is similar to that of the counterpart shown in Fig. 27, and an explanation thereof will thus be omitted.

For the main sector shown in Fig. 49A, a Galois field $GF(2^8)$ Reed-Solomon code (141, 123, 19) is used as the outer error correcting code. In this case, the recording bit rate of the main data is $768 \text{ bits} \times 123 \text{ sync blocks} \times 10 \text{ tracks} \times 30 \text{ Hz}$ is 28.339 Mbps.

According to the configuration shown in 49A, as well as in that shown in Fig. 24A, bit errors caused by a scratch extending for a maximum of $650 \mu\text{m}$ in the longitudinal direction of the track can be corrected. Additionally, by interleaving outer error correcting codes over a plurality of tracks, for example, 16 tracks, continuous errors for two tracks can be corrected.

Figs. 53 through 55 illustrate an example of the arrangement of sync blocks on the magnetic tape 21 in which error correcting codes are interleaved on 16 planes over 16 tracks according to the method shown in Fig. 33.

Figs. 56 through 58 illustrate an example of the arrangement of sync blocks on the magnetic tape 21 in which error correcting codes are interleaved on 16 planes over 16 tracks according to the method shown in Fig. 34.

Parity C2 may be disposed at the end of the track, as shown in Fig. 49B, or may be divided and disposed at the head and the end of the track, as shown in Fig. 49C.

Fig. 59A illustrates still another example of the configuration of the main sector. In this example, the length of one sync block is 114 bytes, and 135 sync blocks form the main sector. Among the 135 sync blocks, 118 sync blocks serve as the main data, and 17 sync blocks serve as parity C2.

In each sync block, the length of the sync is two bytes, and the length of the ID is three bytes. The length of the main data is 99 bytes, and the length of parity C1 is 10 bytes. As the outer error correcting code, a Galois field (2^8) Reed-Solomon code (135, 118, 18) is used. With this arrangement, bit errors caused by a scratch extending for a maximum of about 630 μm in the longitudinal direction of the track can be corrected. Additionally, by interleaving the

outer error correcting codes over a plurality of tracks, for example, 16 tracks, on the magnetic tape 21, continuous errors over two tracks can be corrected.

The error correcting performance of the example shown in Fig. 59A is slightly lower than that of the example shown in Fig. 24A. On the other hand, the recording rate of the main data can be improved to $792 \text{ bits} \times 118 \text{ sync blocks} \times 10 \text{ tracks} \times 30 \text{ Hz} = 28.0368 \text{ Mbps}$.

As in the example shown in Figs. 24B and 24C, parity C2 may be located at the end of the track, as shown in Fig. 59B, or may be divided and located at the head and the end of the track, as shown in Fig. 59C.

Fig. 60 illustrates an example of the sector arrangement in each track when the main sector is formed as shown in Fig. 59. The basic configuration of the sector arrangement is similar to that shown in Fig. 8. Accordingly, the recording and reading operation can be performed by the recording system shown in Fig. 3 and the reading system shown in Fig. 27. However, in the example shown in Fig. 60, the length of the main sector is 128250 bits ($= 114 \times 8 \times 135 \times 25/24 \text{ bits}$), and the length of the postamble is 1875 bits.

Figs. 61A through 61C illustrate a further example of the configuration of the main sector. As in the example shown in Figs. 59A through 59C, the length of one sync block

is 114 bytes. The number of sync blocks of the main data in one track is 118, and that of the parity C2 is 17. Accordingly, as the outer error correcting code, a Galois field (2^8) Reed-Solomon code (135, 118, 18) is used.

In this example, the length of the main data of one sync block is 97 bytes, and that of the parity C1 is 12 bytes. With this configuration, it is possible to correct bit errors caused by a scratch extending for a maximum of 630 μm in the longitudinal direction of the track. Additionally, by interleaving outer error correcting codes over a plurality of tracks, for example, 16 tracks, on the magnetic tape 21, continuous errors over two tracks can be corrected. The error correcting performance of the inner error correcting codes is improved over the example shown in Figs. 24A through 24C. The probability that the data is not correctly decoded is indicated by curve A in Fig. 62. In comparison with curve A shown in Fig. 30, the probability that the data is not correctly decoded is decreased.

The number of parity bits is also increased by 16 bits compared with the example shown in Figs. 24A through 24C. Accordingly, the probability Q_{IN} that the data is erroneously corrected is expressed by the following equation.

$$Q_{IN} = 1.3E^{-29}$$

However, in comparison with the example shown in Figs. 24A through 24C, the error correcting performance is improved

with an impairment of the recording rate, which results in
 $776 \text{ bits} \times 118 \text{ sync blocks} \times 10 \text{ tracks} \times 30 \text{ Hz} = 27.4704$
Mbps.

As is seen from the foregoing description, when recording or reading MPEG-compressed data as discussed above, the following advantages can be offered over the DV format.

In case of the occurrence of spontaneous clogging generated during a recording operation (recording errors), about one track of such an error can be corrected when error correcting codes are interleaved on eight planes over eight tracks, and about two tracks of such an error can be corrected when error correcting codes are interleaved on 16 planes over 16 tracks. The resistance to reading errors caused by splices on the recorded tape can be enhanced. That is, if a new track is spliced too close to the previous track, the previous track becomes smaller than it should be. Such an error can be corrected. Also, the error resistance to a scratch in the longitudinal direction of tape is higher than that of the DV format by about 1.8 times or greater. The ID is included together with the main data in Reed-Solomon codes, and thus, the reliability of continuity checking of the sync block numbers and track numbers contained in the ID can be improved. Accordingly, the probability that data cannot be correctly decoded during a reading operation is much lower than that of the DV format.

The length of the sync block is 111 bytes or 114 bytes, which is compatible with the length of a transport stream in the MPEG method when it is disposed in the sync blocks. Thus, the reading and recording of transport streams transferred via a digital interface, which is one of the standard formats, can be easily performed. Additionally, since 24-25 conversion used in the DV format is also applicable to the magnetic tape format of the present invention, the corresponding system can be easily constructed based on the DV system.

It can thus be understood that the present invention is effective as one format for recording and reading MPEG compressed data on and from, not only digital video cassettes, but also tape media.

The above-described series of processing may be executed by hardware or software. If software is used, it can be installed from a recording medium into a computer which contains special hardware integrating the corresponding software program or into a computer, for example, a general-purpose computer, which executes various functions by installing various programs.

Such a recording medium may be formed of a package medium, which is distributed to the user separately from the magnetic tape recording/reading apparatus, such as a magnetic disk 31 (including a floppy disc), an optical disc

32 (including compact disc read only memory (CD-ROM) and a digital versatile disk (DVD)), a magneto-optical disk 33 (including an mini disk (MD)), or a semiconductor memory 34. The recording medium may also be formed of a ROM or a hard disk on which the program is recorded, which can be provided to the user while being installed in the magnetic disc recording/reading apparatus.

It is not essential that the steps forming the program recorded on a recording medium be executed chronologically according to the order discussed in this specification. Alternatively, they may be executed concurrently or individually.